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CONFIRMATION NO. 3056

<b>SERIAL NUMBER</b> 10/519,799	<b>FILING OR 371(c) DATE</b> 12/02/2005 <b>RULE</b>	<b>CLASS</b> 438	<b>GROUP ART UNIT</b> 2891	<b>ATTORNEY DOCKET NO.</b> T&A-135
<b>APPLICANTS</b> Satoshi Sakai, Yokohama, JAPAN; Satoshi Yamamoto, Takarazuka, JAPAN; Atsushi Hiraiwa, Higashimurayama, JAPAN; Ryoichi Furukawa, Ome, JAPAN;				
<b>** CONTINUING DATA *****</b> This application is a 371 of PCT/JP02/08284 08/15/2002				
<b>** FOREIGN APPLICATIONS *****</b>				
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Examiner's Signature <i>V. J. [Signature]</i> Initials <i>VJ</i>		<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWING</b> 45	<b>TOTAL CLAIMS</b> 19
<b>INDEPENDENT CLAIMS</b> 2				
<b>ADDRESS</b> Mattingly Stanger & Malur 1800 Diagonal Road Suite 370 Alexandria, VA22314				
<b>TITLE</b> Semiconductor integrated circuit device and method for fabricating the same				
<b>FILING FEE RECEIVED</b> 1030	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	